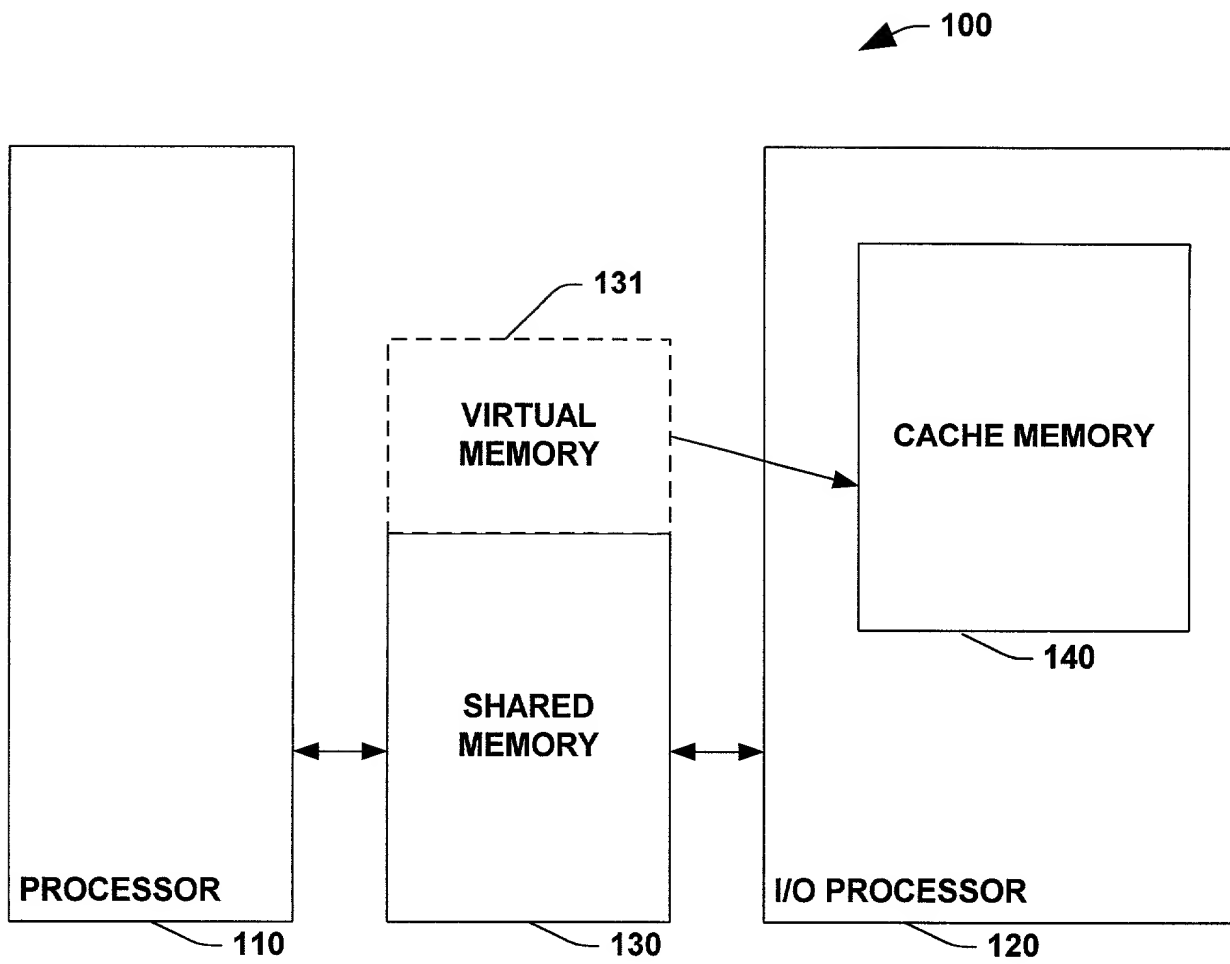
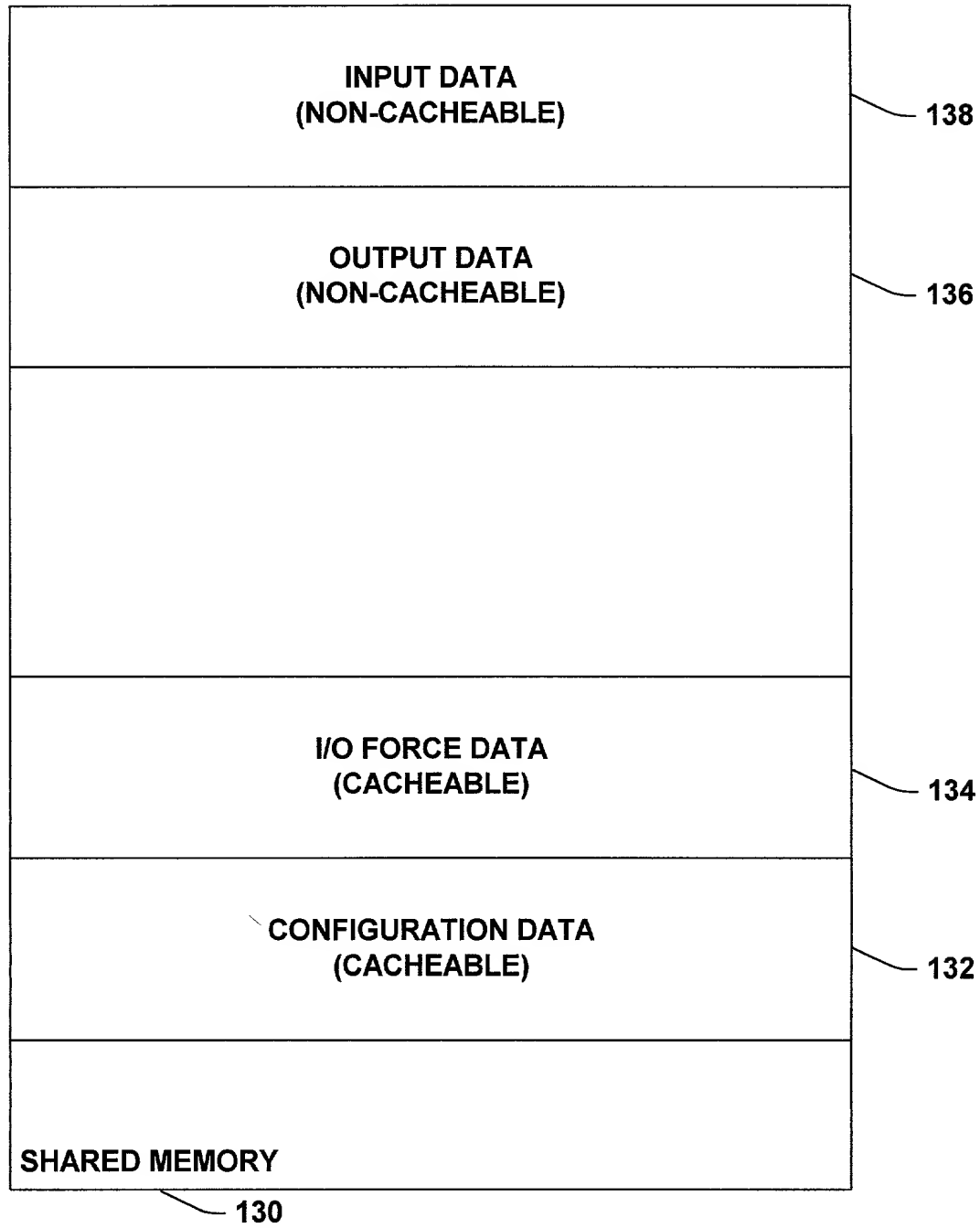


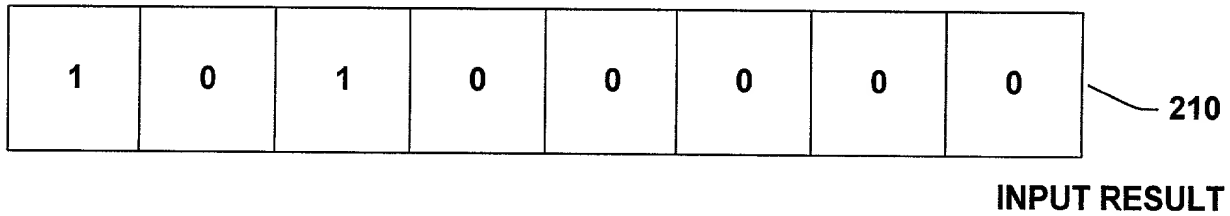
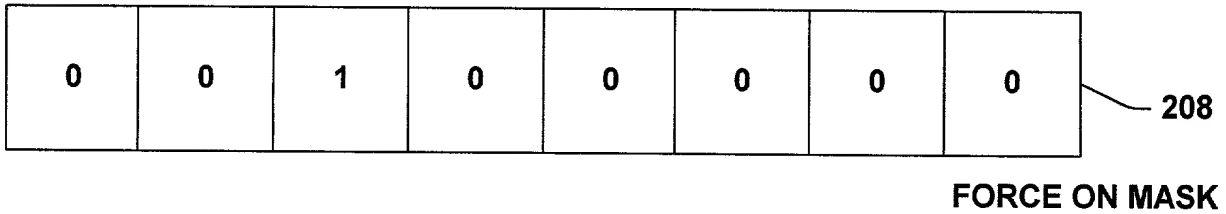
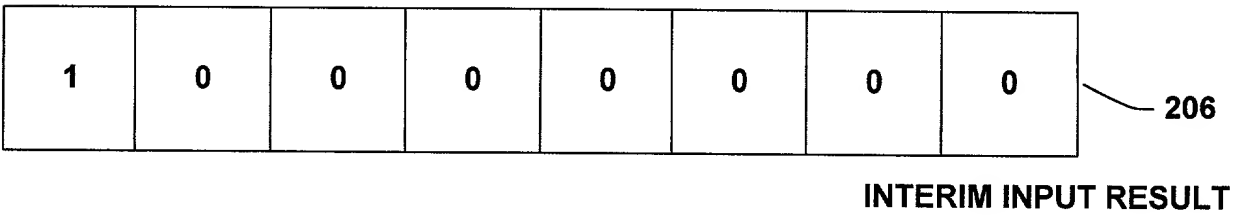
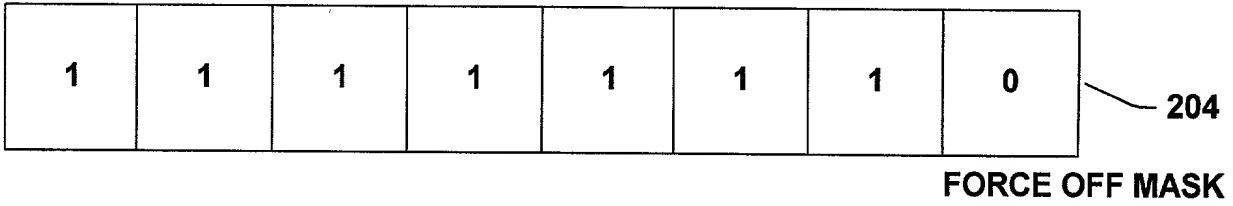
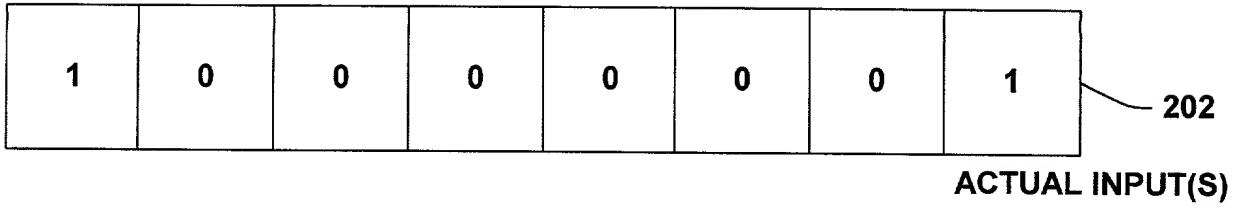
FIG. 1 is a block diagram of a system 100. The system 100 includes a processor 110, a shared memory 130, a virtual memory 131, a cache memory 140, and an I/O processor 120. The processor 110 is connected to the shared memory 130. The shared memory 130 is connected to the I/O processor 120. The virtual memory 131 is connected to the cache memory 140. The cache memory 140 is connected to the I/O processor 120.



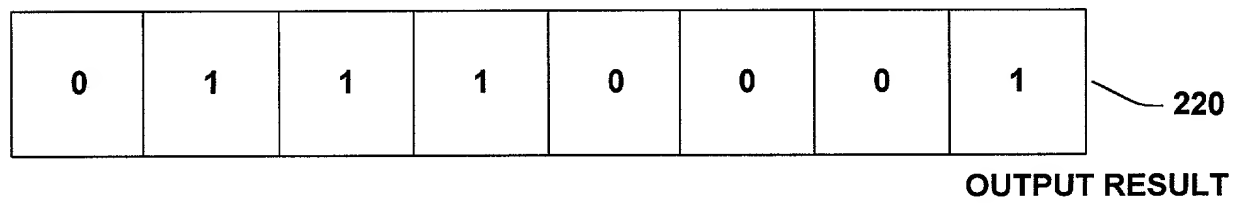
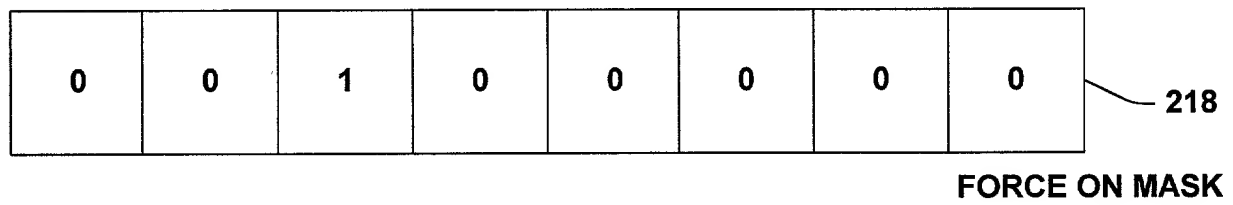
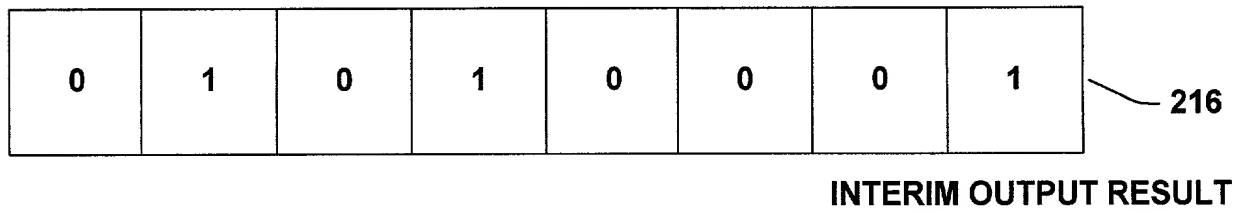
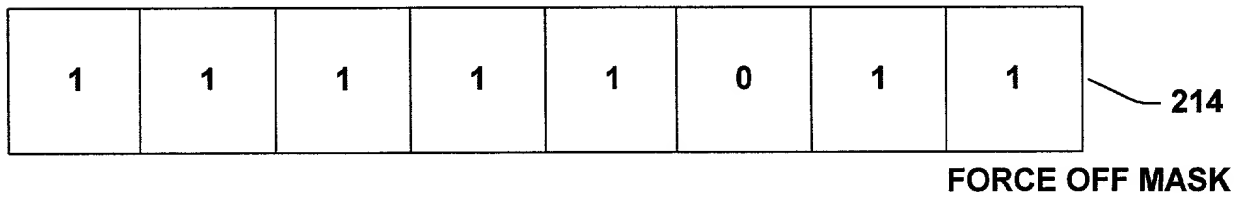
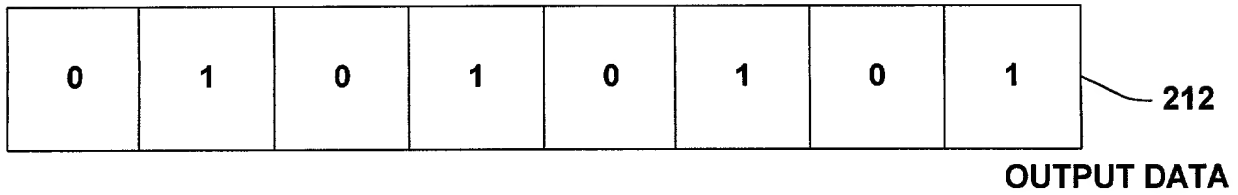
**FIG. 1**



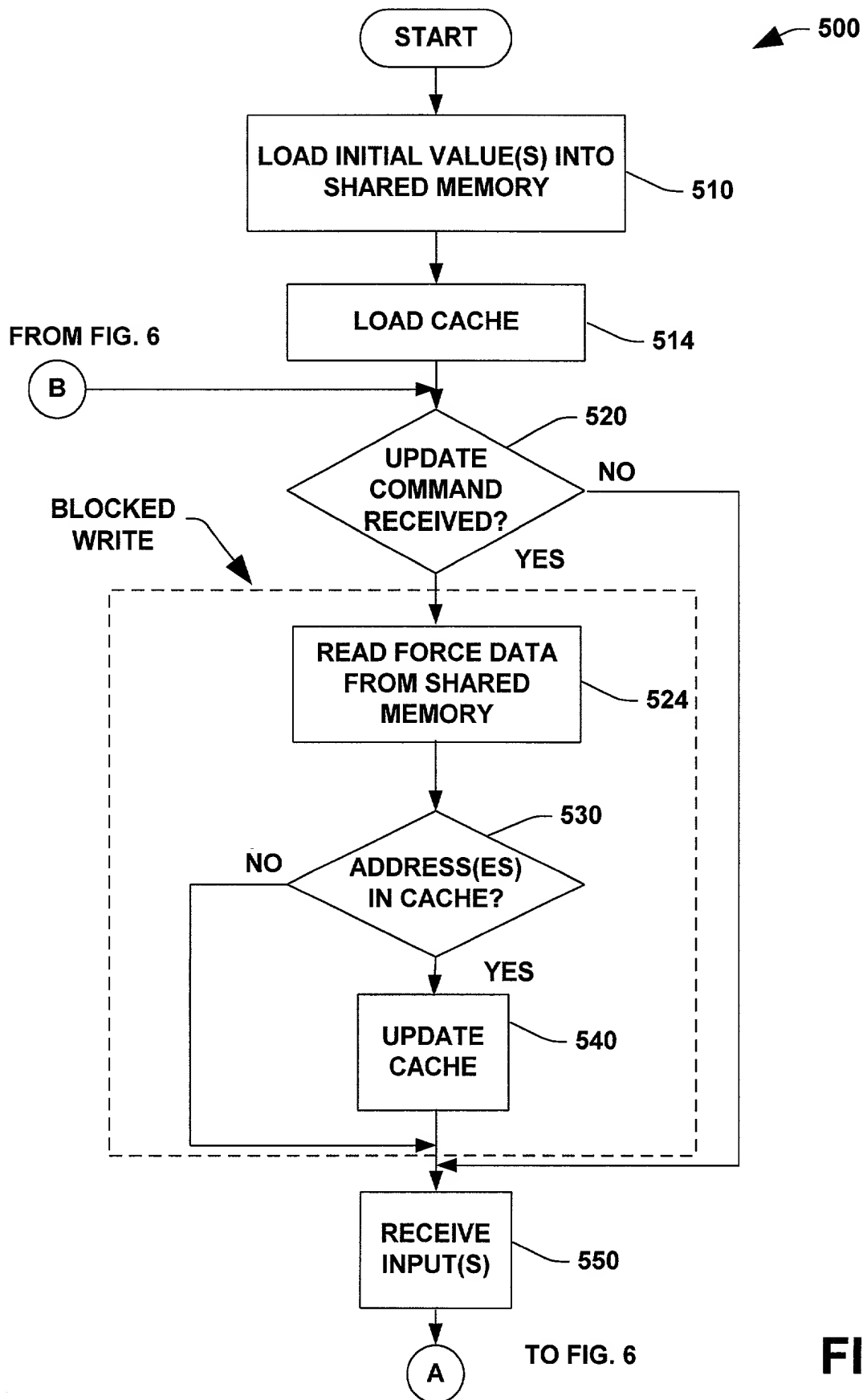
**FIG. 2**



**FIG. 3**



**FIG. 4**



**FIG. 5**

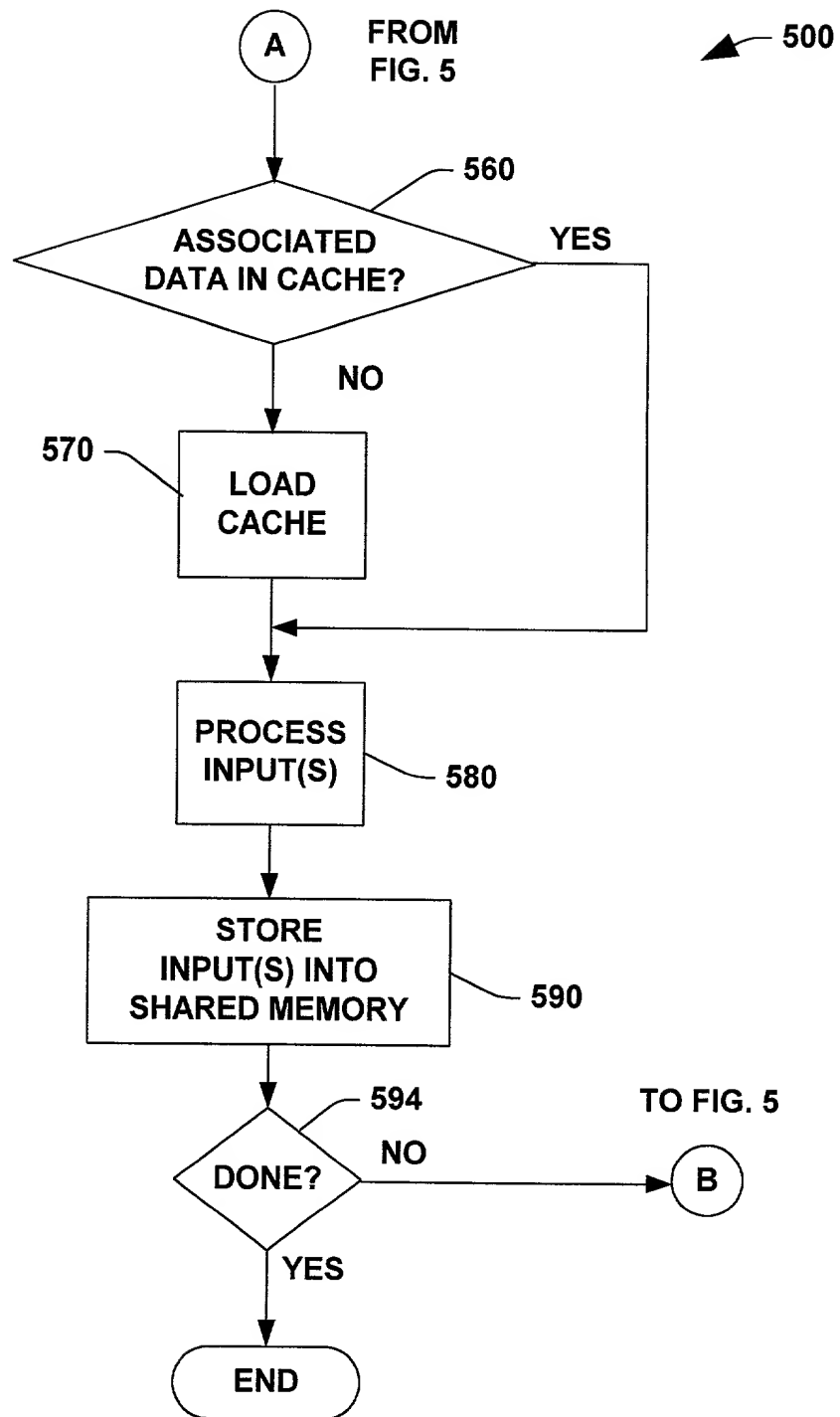


FIG. 6

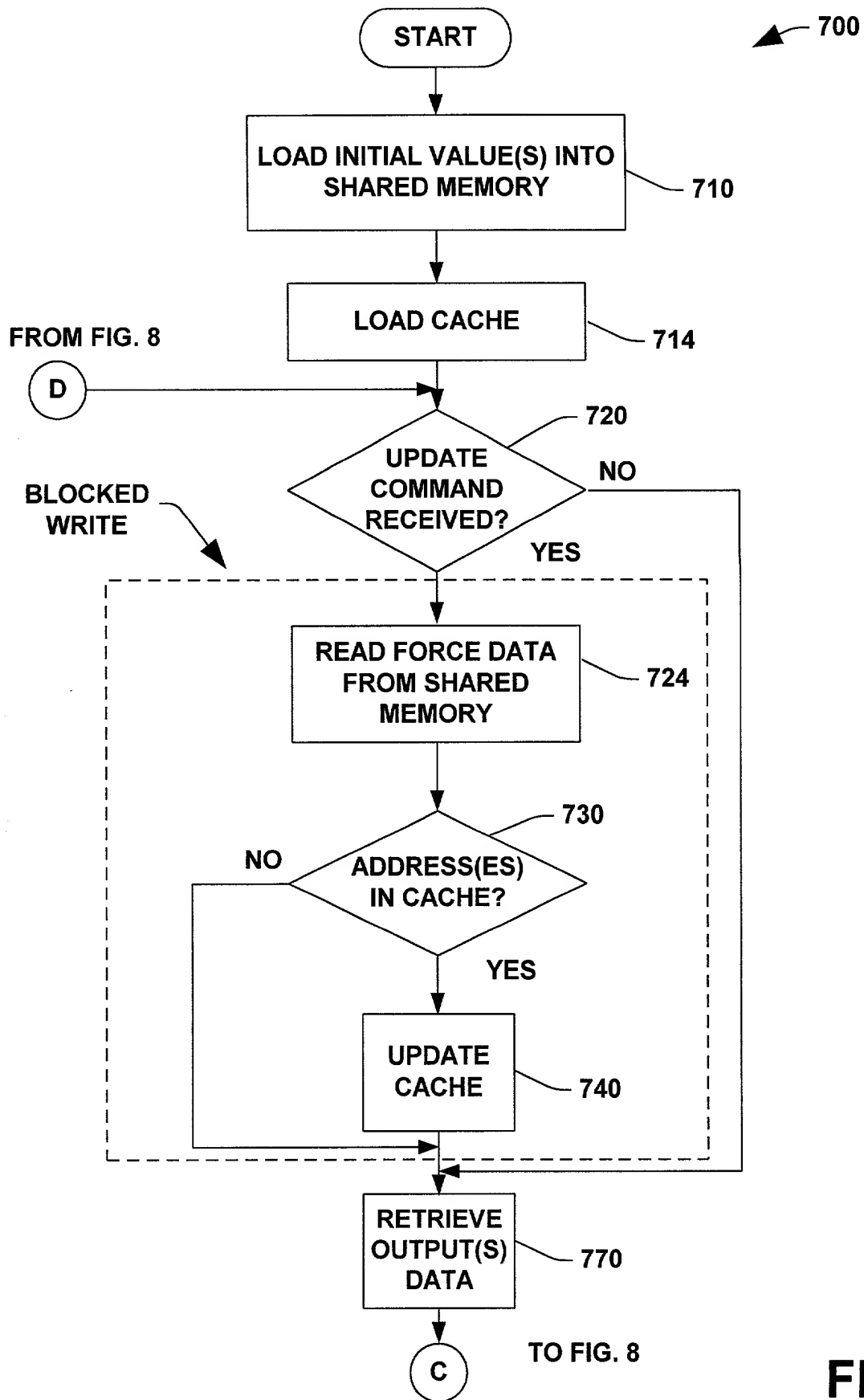
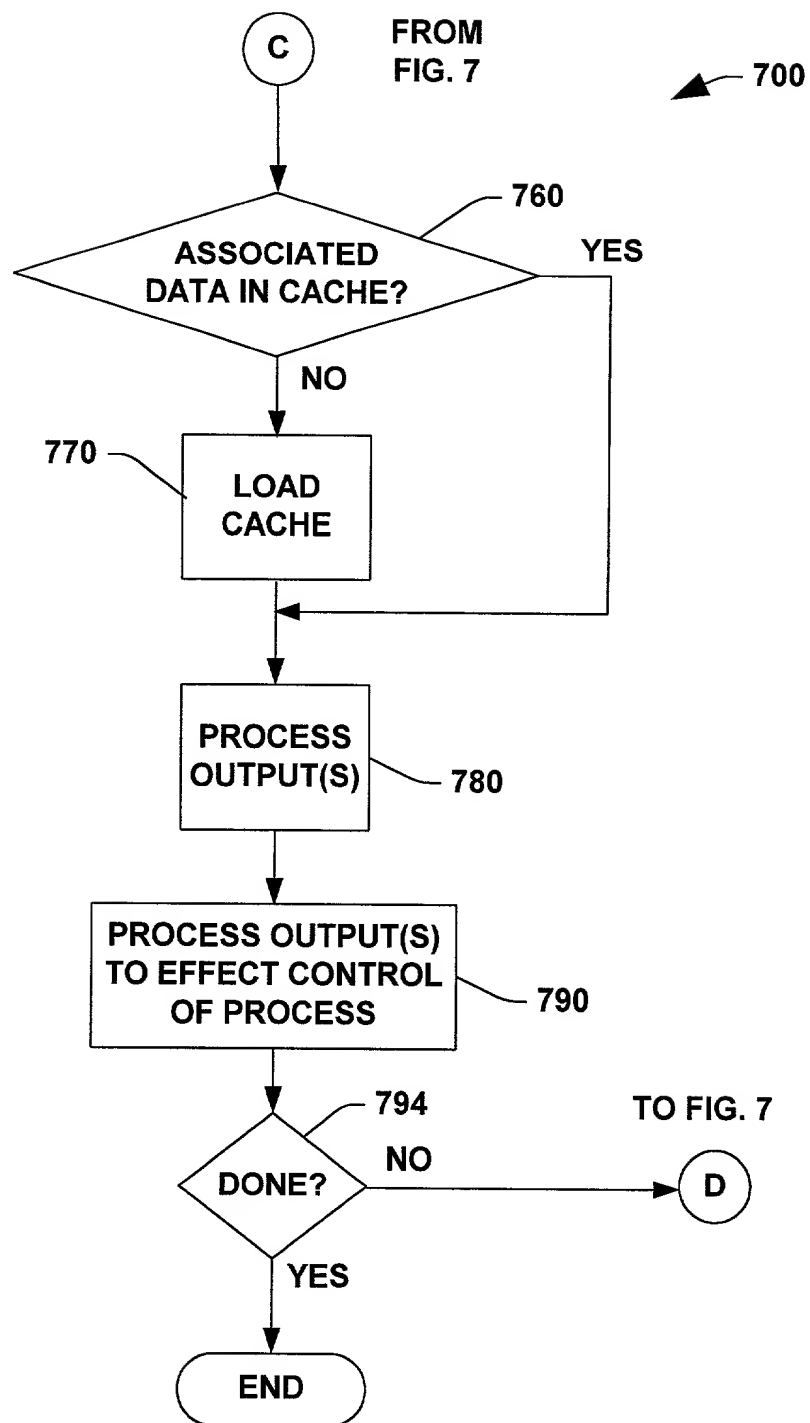


FIG. 7



**FIG. 8**

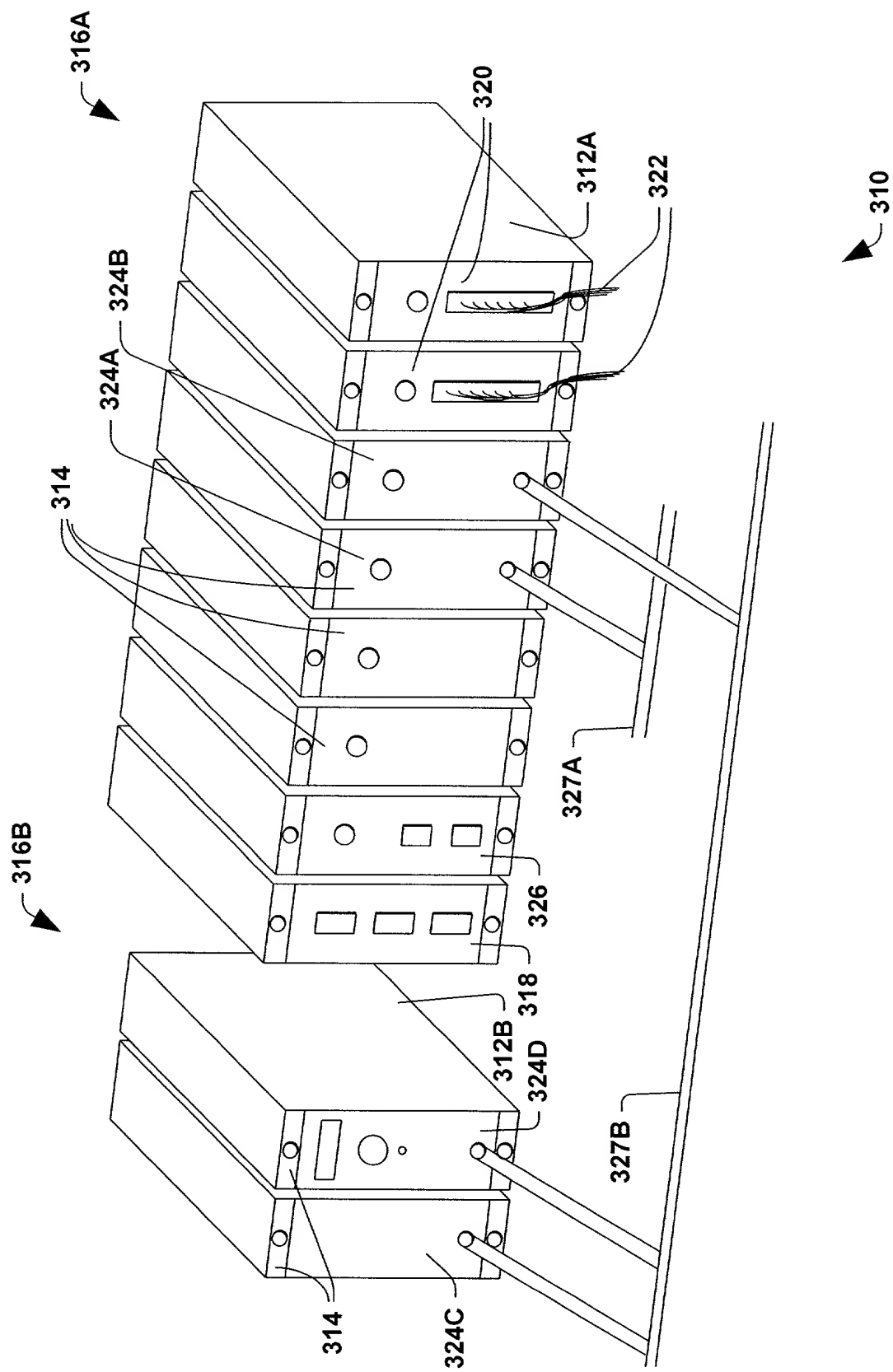
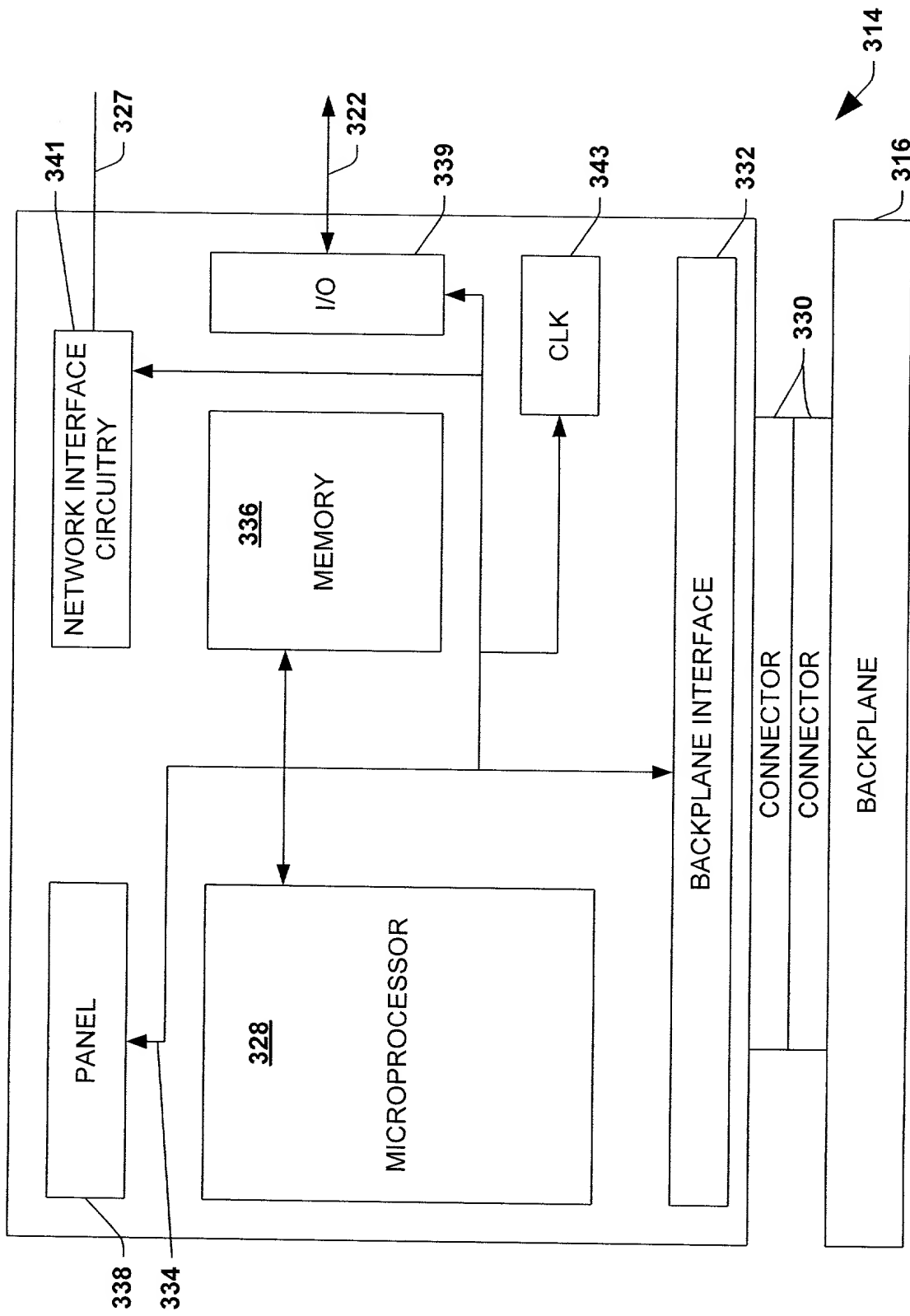


FIG. 9



**FIG. 10**